

The Non-Invasive Attack Testing Workshop (NIAT 2011) Accepted Papers

- **Choosing Distinguishers for Differential Power Analysis Attacks**  
*Elisabeth Oswald, Luke Mather, and Carolyn Whitnall*  
University of Bristol, Department of Computer Science
- **Non-invasive Trigger-free Fault Injection Method Based on Intentional Electromagnetic Interference**  
*Yu-ichi Hayashi, Naofumi Homma, Takeshi Sugawara, Takaaki Mizuki, Takafumi Aoki, and Hideaki Sone*  
Tohoku University
- **Education and open benchmarking on side-channel analysis with the DPA contests**  
*Jean-Luc Danger, Guillaume Duc, Sylvain Guilley and Laurent Sauvage*  
DPA Contest
- **Side-Channel Attack Standard Evaluation Board SASEBO-W for Smartcard Testing**  
*Toshihiro Katashita, Yohei Hori, Hirofumi Sakane, and Akashi Satoh*  
National Institute of Advanced Industrial Science and Technology (AIST)
- **Novel Applications of Wavelet Transforms based Side-Channel Analysis**  
*Youssef Souissi<sup>1</sup>, M.Aziz el Aabid<sup>1,2</sup>, Jean-Luc Danger<sup>1,3</sup>, Sylvain Guilley<sup>1,3</sup>, Nicolas Debande<sup>1,4</sup>*  
<sup>1</sup> Telecom Paris-Tech, <sup>2</sup> Université Paris 8, <sup>3</sup> Secure-IC SAS, <sup>4</sup> Morpho
- **Efficient FPGA Implementation of dual-rail countermeasures using Stochastic Models**  
*Shivam Bhasin, Sylvain Guilley, Youssef Souissi, Jean-Luc Danger*  
Telecom Paris-Tech
- **A testing methodology for side-channel resistance validation**  
*Gilbert Goodwill, Benjamin Jun, Josh Jaffe, and Pankaj Rohatgi*  
Cryptography Research Inc.
- **Efficient side-channel testing for public key algorithms: RSA case study**  
*Josh Jaffe<sup>1</sup>, Pankaj Rohatgi<sup>1</sup>, and Marc Witteman<sup>2</sup>*  
<sup>1</sup> Cryptography Research Inc. <sup>2</sup> Riscure

- **An Equidistant Message Power Attack Using Restricted Number of Traces on Reduction Algorithm**

*Jong-Yeon Park<sup>1</sup>, Dong-Guk Han<sup>1</sup>, Okyeon Yi<sup>1</sup>, and Dooho Choi<sup>2</sup>*

<sup>1</sup> Cryptography and Information Security Institute(CISI), Department of Mathematics  
Kookmin University

<sup>2</sup> Electronic and Telecommunication Research Institute(ETRI)

- **Test Apparatus for Side-Channel Resistance Compliance Testing**

*Michael Hutter, Jörn-Marc Schmidt, Thomas Plos, and Mario Kirschbaum*

Institute for Applied Information Processing and Communications (IAIK), Graz University of  
Technology

- **Practical Electro-Magnetic Analysis**

*Fred de Beer, Marc Witteman, and Bartek Gedrojc*

Riscure

- **Evaluation Tools for Side-Channel Attacks: An Overview**

*François-Xavier Standaert*

UCL Crypto Group, Université catholique de Louvain